



#7 Response

M. Brunson

9/25/02 PATENT  
Customer No. 22,852

Attorney Docket No. 08245.0027

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: )  
)  
Jun Dong KIM et al. ) Group Art Unit: 2814  
)  
Serial No.: 09/892,878 ) Examiner: S. Rao  
)  
Filed: June 28, 2001 )  
)  
For: METHOD FOR FORMING GATE )  
ELECTRODES IN A )  
SEMICONDUCTOR DEVICE )  
USING FORMED FINE PATTERNS )

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Commissioner for Patents and Trademarks  
Washington, DC 20231

Sir:

**REQUEST FOR RECONSIDERATION**

In response to the Office Action dated June 20, 2002, Applicants respectfully reconsideration of the above-identified application based on the following remarks:

**REMARKS**

In the Office Action dated June 20, 2002, the Examiner rejected claims 6-10 under 35 U.S.C. § 112, second paragraph and rejected claims 1-10 under 35 U.S.C. § 103(a) as being unpatentable over Ye et al. (U.S. Patent No. 6,080,529) in view of Lau et al. (U.S. Patent No. 5,173,542).

Based on the following remarks, Applicants respectfully traverse the rejections under 35 U.S.C. § 112, second paragraph and 35 U.S.C. § 103(a).

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